

TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	10/672,895	
	Filing Date	September 26, 2003	
	First Named Inventor	Mira Ben-Tzur	
	Art Unit	not yet known	
	Examiner Name	not yet known	
Total Number of Pages in This Submission	6 **	Attorney Docket Number	10002.000810 (PM02006-2)

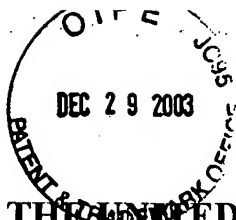
ENCLOSURES (check all that apply)		
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Remarks		** does not include pages of listed references

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Patrick D. Benedicto, Reg No. 40,909 OKAMOTO & BENEDICTO LLP
Signature	<i>Patrick Benedicto</i>
Date	December 23, 2003

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Typed or printed name	Patrick D. Benedicto		
Signature	<i>Patrick Benedicto</i>	Date	December 23, 2003

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Mira Ben-Tzur, et al.

Serial No. 10/672,895

Filed: September 26, 2003

Title: INTEGRATED CIRCUIT WITH
IMPROVED RC DELAY

Examiner: not yet known

Art Unit: not yet known

Atty. Dkt. No.: 10002.000800 (PM02006-2)

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Under 37 C.F.R. §1.56 and §§1.97-98

Sir:

Pursuant to the provisions of 37 CFR 1.56 and §§1.97-98, enclosed herewith is form PTO/SB/08 (substitute for form 1449A/PTO) listing references which may be material to the examination of this application, and with respect to which there may be a duty to disclose. The filing of this Information Disclosure Statement shall not be construed as a representation regarding the completeness of the list of references, or that inclusion of a reference in this list is an admission that it is prior art or is pertinent to this application, or that a search has been made, or as an admission that the information listed is, or may be considered to be, material to patentability, or that no other material information exists, and shall not be construed as an admission against interest in any manner.

A copy of each listed reference is enclosed herewith, unless no such copy is required pursuant to 37 CFR § 1.98(d).

- ☐ This application relies, under 35 U.S.C. § 120, on the earlier filing date of prior application Serial No. [SERIAL NUMBER], filed on [FILING DATE], and the references cited therein are hereby listed but are not required to be provided in this application under 37 CFR § 1.98(d).

Pursuant to 37 CFR §1.97, this Information Disclosure Statement is being submitted under the section indicated below.

- ☒ 37 CFR §1.97(b).
- ☐ 37 CFR §1.97(c). If so, then either:
- ☐ Applicant certifies pursuant to 37 CFR § 1.97(e) that either:
- ☐ each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Statement; OR
- ☐ no item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the person signing this certification after making reasonable inquiry, was known to any individual designated under 37 CFR § 1.56 (c) more than three months prior to the filing of this Statement.
- OR
- ☐ Applicant submits the petition fee of \$180 as set forth in 37 CFR § 1.17(p).
- ☐ 37 CFR § 1.97(d). If so, then both:
- ☐ Applicant certifies pursuant to 37 CFR § 1.97(e) that either:
- ☐ each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Statement; OR
- ☐ no item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the person signing this certification after making reasonable inquiry, was known to any individual designated under 37 CFR § 1.56(c) more than three months prior to the filing of this Statement.
- AND
- ☐ Applicant submits the petition fee of \$180 as set forth in 37 CFR § 1.17(p).

Consideration of the listed references and favorable action are respectfully solicited.

Respectfully submitted,

Mira Ben-Tzur, et al.

Dated: December 23, 2003

By: Patrick D. Benedicto

Patrick D. Benedicto

Attorney For Applicant(s)

Reg. No. 40,909

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Signature:	<u>Patrick D. Benedicto</u>		
Typed or Printed Name:	Patrick D. Benedicto	Dated:	December 23, 2003
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PTO/SB/08a (08-03)

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Substitute for form 1449A, PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 2

Complete if Known

Application Number	10/672,895
Filing Date	September 26, 2003
First Named Inventor	Mira Ben-Tzur
Art Unit	not yet known
Examiner Name	not yet known
Attorney Docket Number	10002.000810 (PM02006-2)

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
	A	US- 5,668,398	Sept. 16, 1997	Haverman, et al.	
	B	US- 5,918,149	Jun. 29, 1999	Besser, et al.	
	C	US- 5,963,830	Oct. 5, 1999	Wang, et al.	
	D	US- 6,120,844	Sep. 19, 2000	Chen, et al.	
	E	US- 6,204,200	Mar. 20, 2001	Shieh, et al.	
	F	US- 6,228,761	May 8, 2001	Ngo, et al.	
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FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				

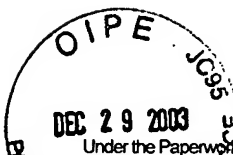
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449B/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 2 of 2

Complete if Known

Application Number	10/672,895
Filing Date	September 26, 2003
First Named Inventor	Mira Ben-Tzur
Art Unit	not yet known
Examiner Name	not yet known
Attorney Docket Number	10002.000810 (PM02006-2)

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	G	SEONG GEON PARK, et al. "A New ALD-TiN/CoSi ₂ Contact Plug Process for Reliable and Low Defect Density Bit-Line Integration in Sub-Quarter Micron Giga-bit DRAM" IEEE 2002, pgs. 282-284; Semiconductor R&D Center, Samsung Electronics Co. LTD., KOREA.	
	H	J.T. WETZEL, et al. "Evaluation of Material Property Requirements and Performance of Ultra-Low Dielectric Constant Insulators for Inlaid Copper Metallization" IEEE 2001, pgs. 4.1.1 - 4.1.3; Int'l Sematech, Austin, TX; Texas Instruments, Dallas, TX; Motorola, Austin, TX; TSMC, Taiwan; IBM, Burlington.	
	I	M. BEN-TZUR, et al. "Integrated of Low-K Dielectrics in The Passivation Level of A1 Based Multilevel Interconnect Technology" submitted for Publication March 2002, (2 pgs); Cypress Semiconductor, San Jose, CA., USA.	
	J	WEI-JEN HSIA, et al. "Flowfill Technology Low Dielectric Constant Materials" Retrieved from the internet:<URL:http://www.trikon.com [printed on 3/29/02] (4 pgs); LSI Logic, CA; Trikon Technologies Ltd.	
	K	BRUCE W. McGAUGHY, et al. "A Simple Method for On-Chip, Sub-Femto Farad Interconnect Capacitance Measurement" IEEE Vol. 18, No. 1 January 1997, pgs. 21 - 23; University of California, Berkeley, CA., USA	
	L	Conference highlights latest data on SiLK resin, expanded Alliance membership, (3 pgs); December 5, 2001; The Dow Chemical Company and Loomis Group; Midland, Michigan, USA.	
	M	INTERCONNECT; The International Technology Roadmap For Semiconductors, pgs. 1 -25, 2001 Edition.	

Examiner
Signature

Date
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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